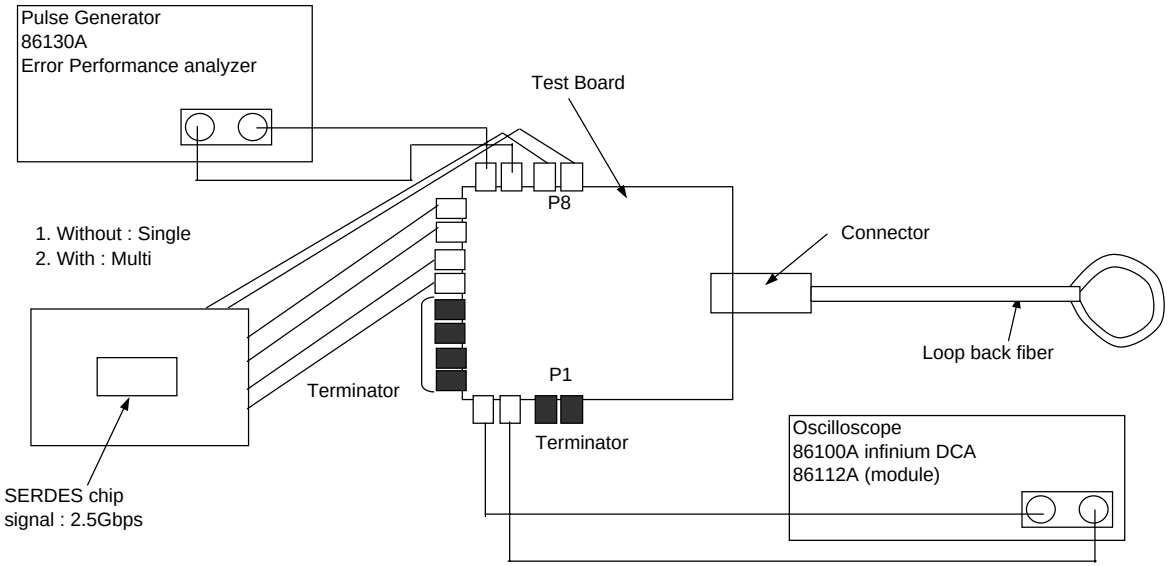
11.Jul.03

EVALUATION REPORT FOR o-MICRO GIGACN

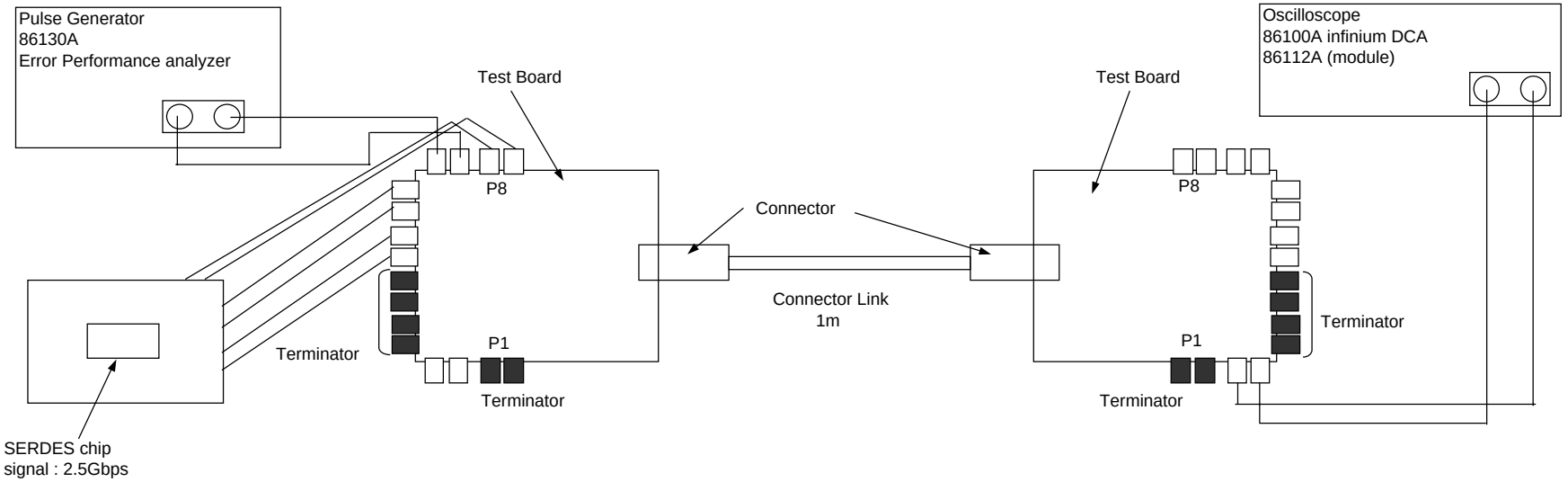
FUJITSU COMPONENT LIMITED

Measurement Method

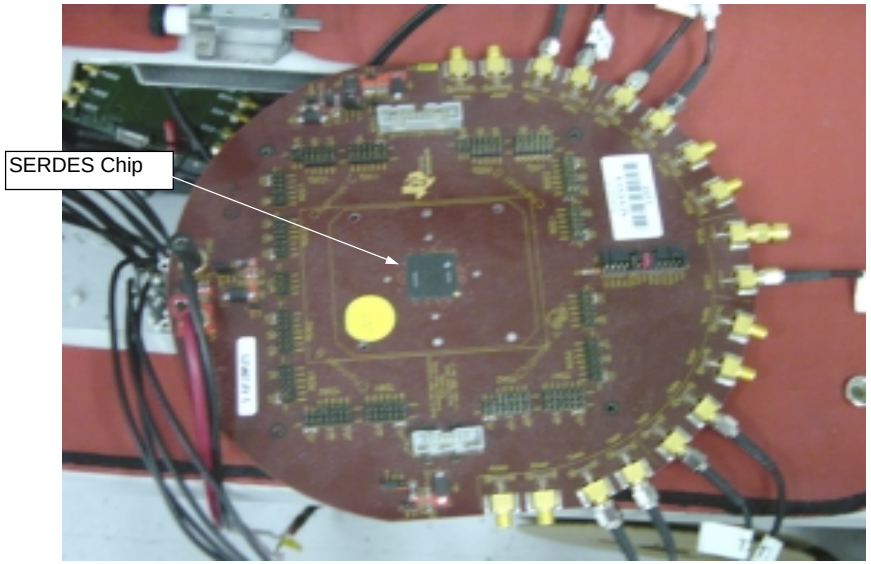
1) Loop back fiber



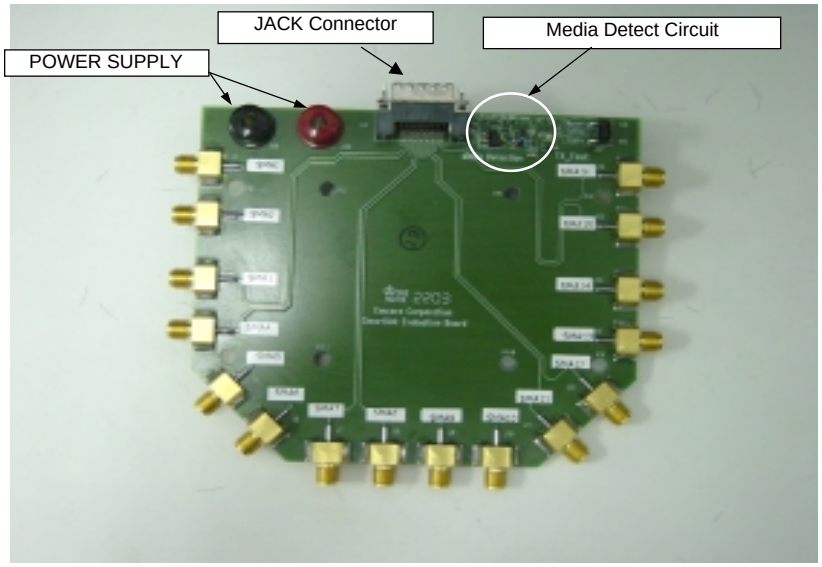
2) Connector Link



SERDES Chip Produce of TEXAS INSTRUMENTS



Evaluation Board

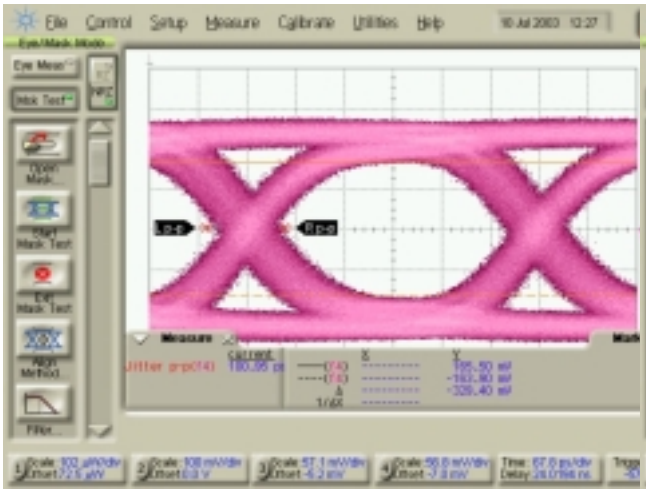


Pin assignment of evaluation board

SMA1	Rx0n
SMA2	Rx0p
SMA3	Rx1n
SMA4	Rx1p
SMA5	Rx2n
SMA6	Rx2p
SMA7	Rx3n
SMA8	Rx3p
SMA9	Tx3n
SMA10	Tx3p
SMA11	Tx2n
SMA12	Tx2p
SMA13	Tx1n
SMA14	Tx1p
SMA15	Tx0n
SMA16	Tx0p

o-MGC 2.5Gbps 1m
Transmit No.1 > Receive No.2

Tx1



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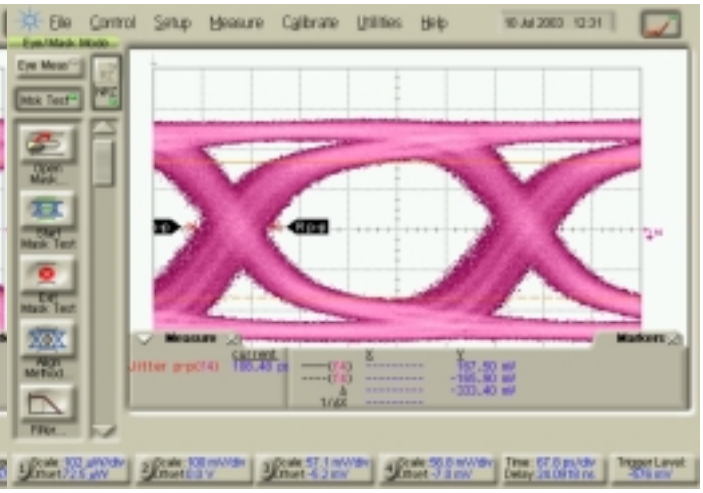

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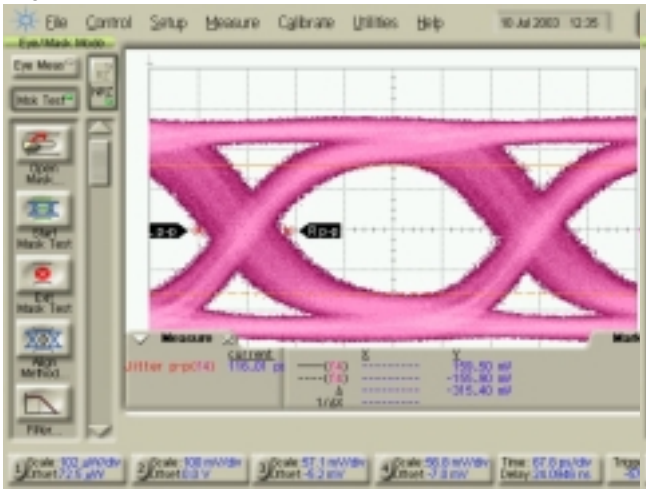

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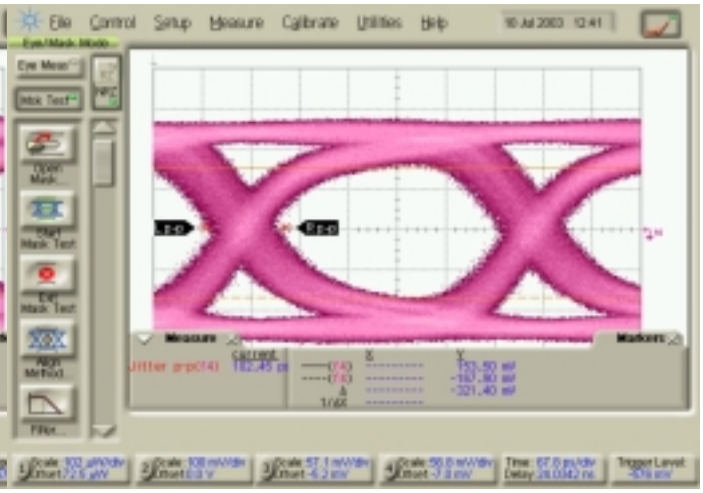
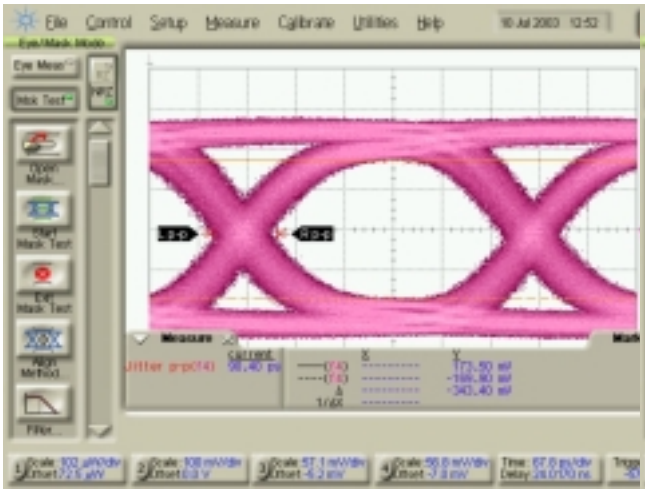


o-MGC 2.5Gbps 1m
Transmit No.2 > Receive No.1

Tx1



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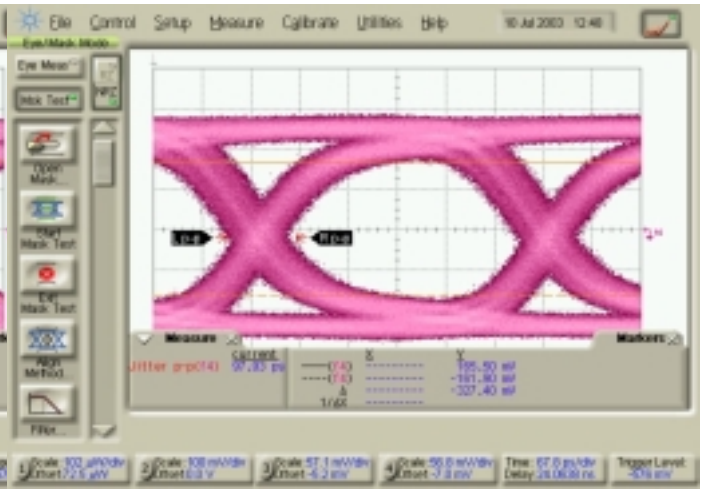

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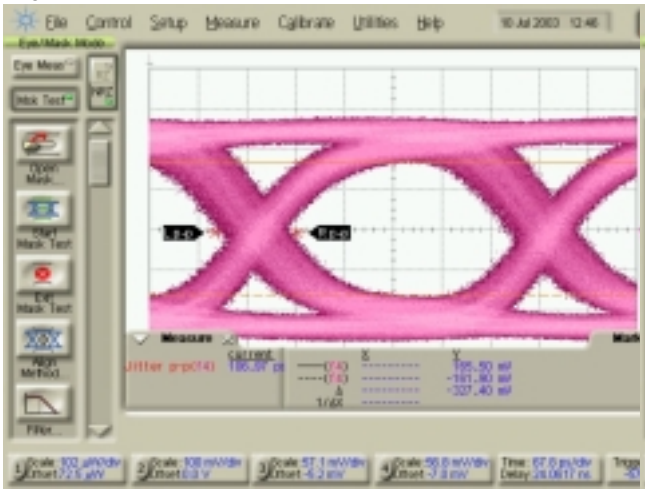

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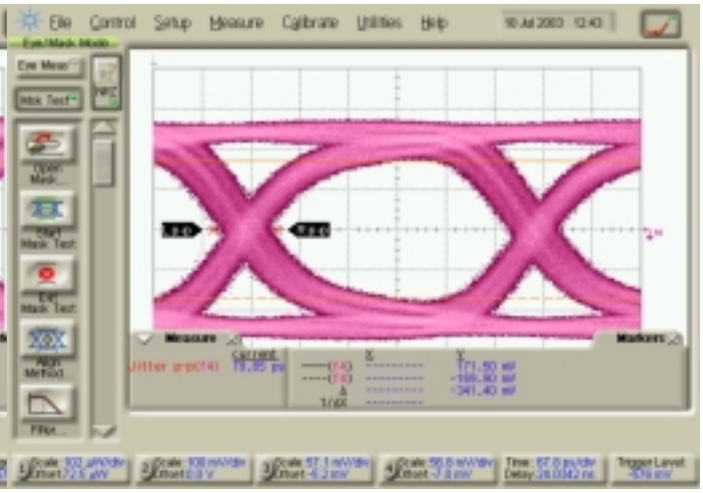
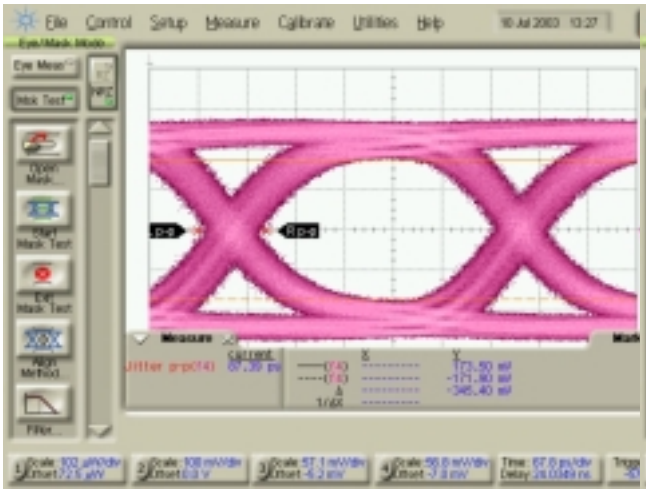


o-MGC 2.5Gbps 1m Multi drive
Transmit No.1 > Receive No.2

Tx1



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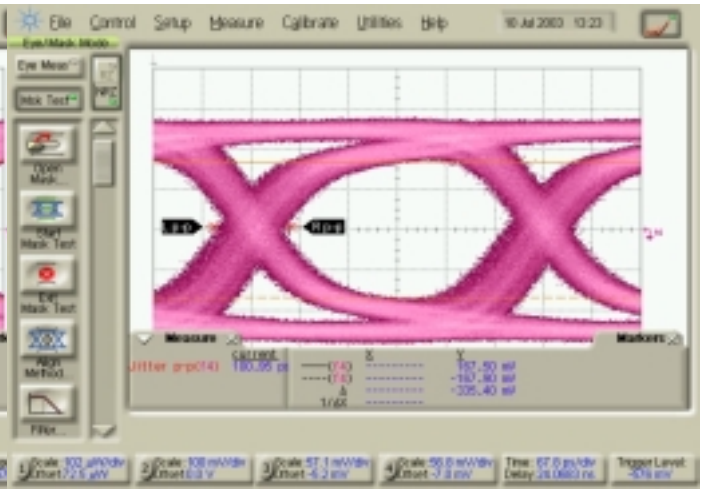

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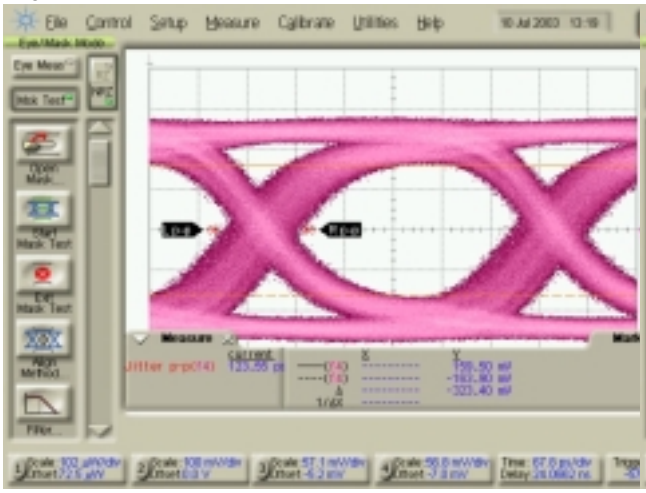

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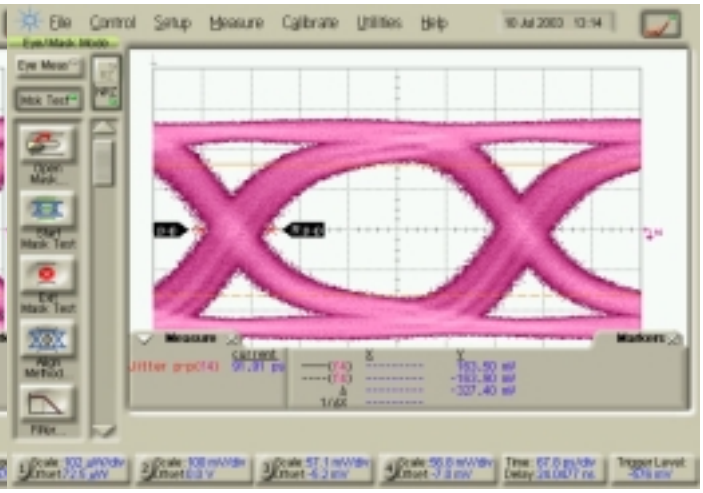
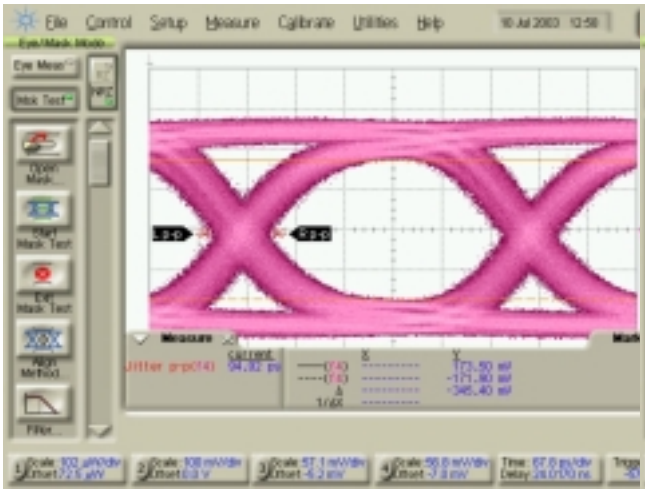


o-MGC 2.5Gbps 1m Multi drive
Transmit No.2 > Receive No.1

Tx1



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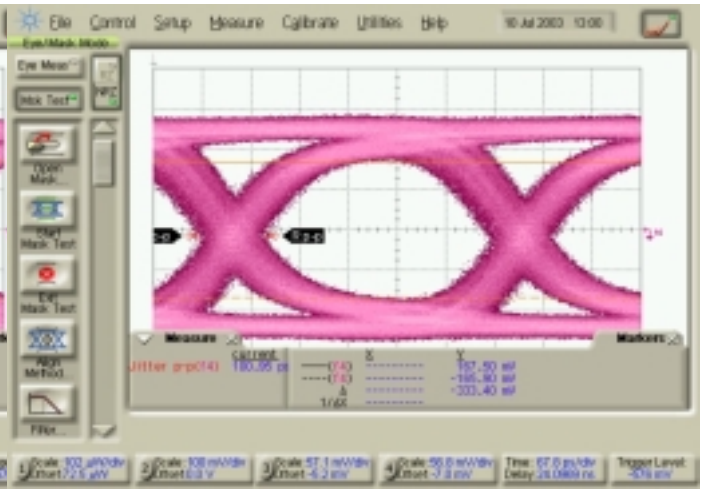

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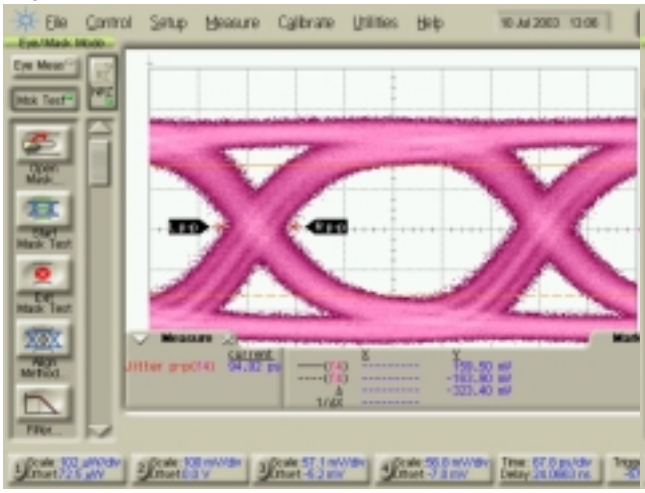

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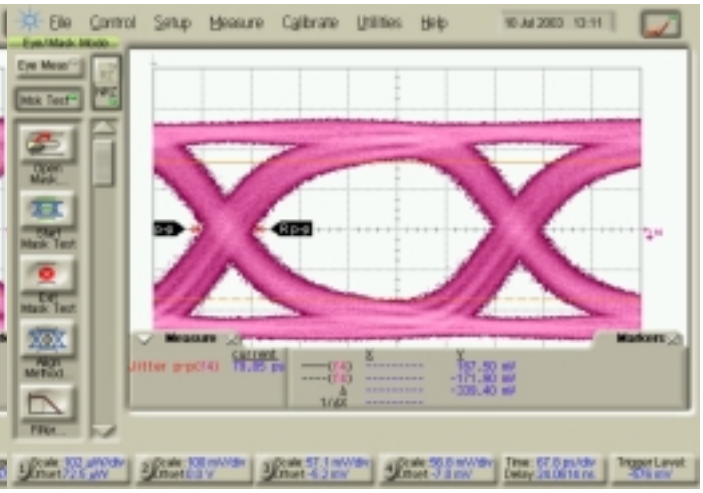
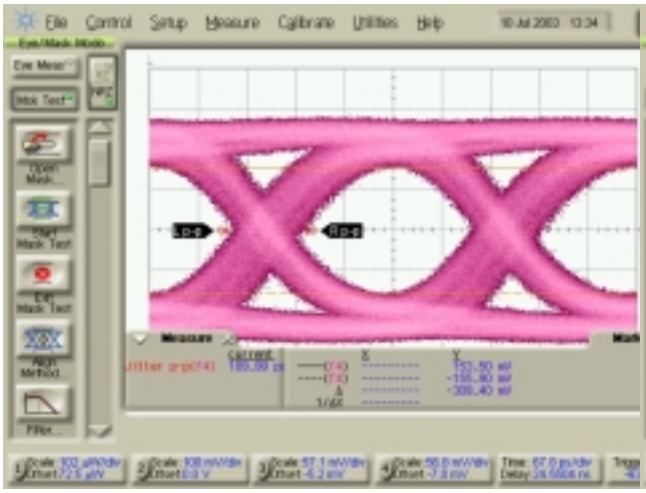


o-MGC 3Gbps 1m

Transmit No.1 > Receive No.2

Tx1



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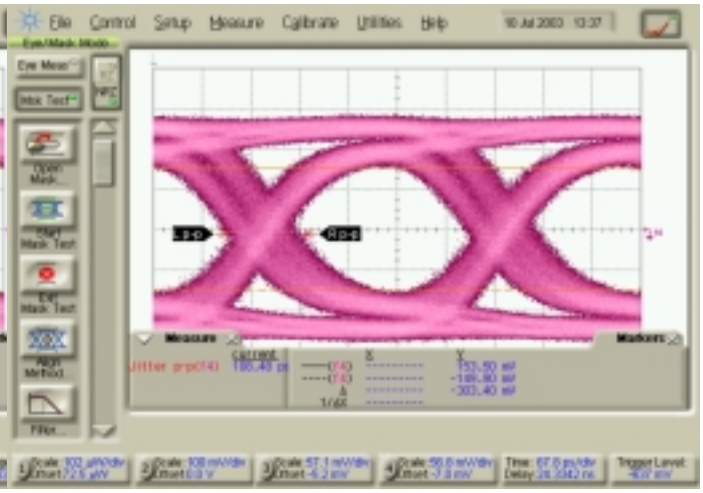

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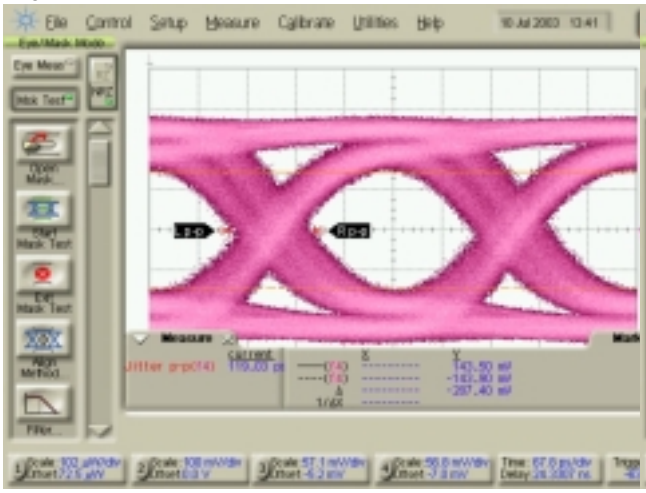

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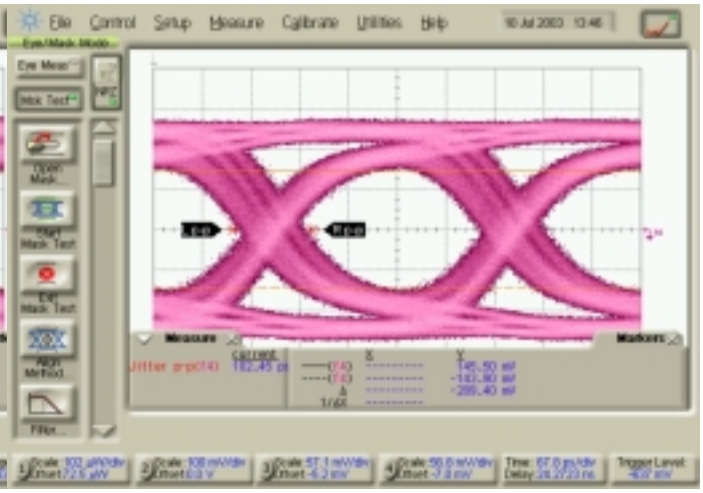
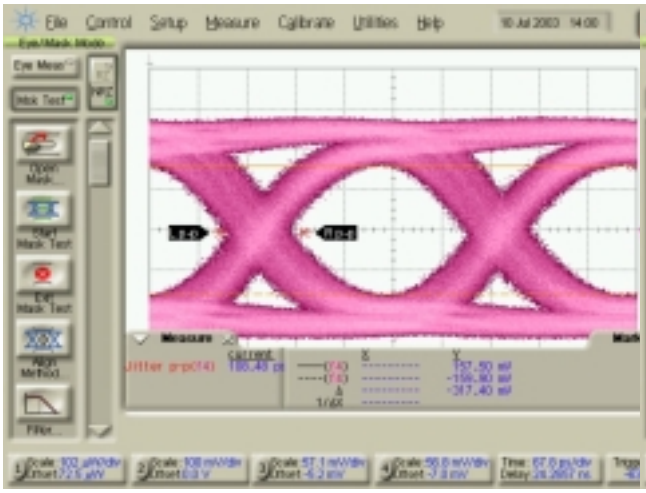


o-MGC 3Gbps 1m

Transmit No.2 > Receive No.1

Tx1



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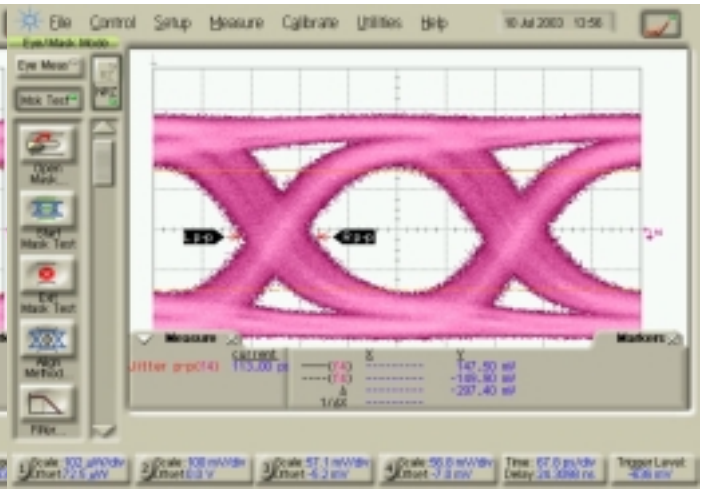

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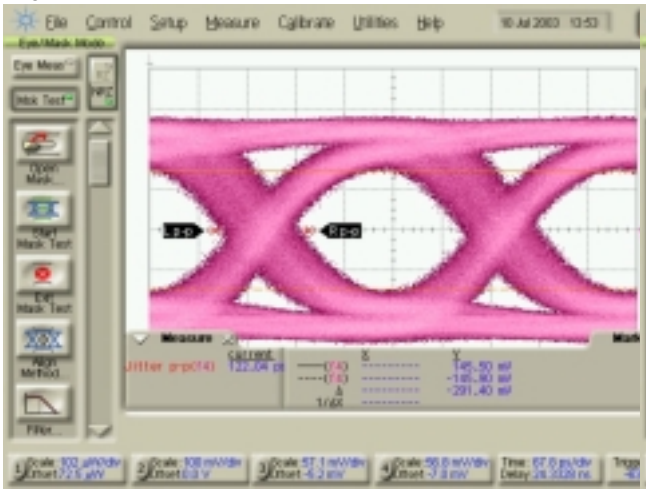

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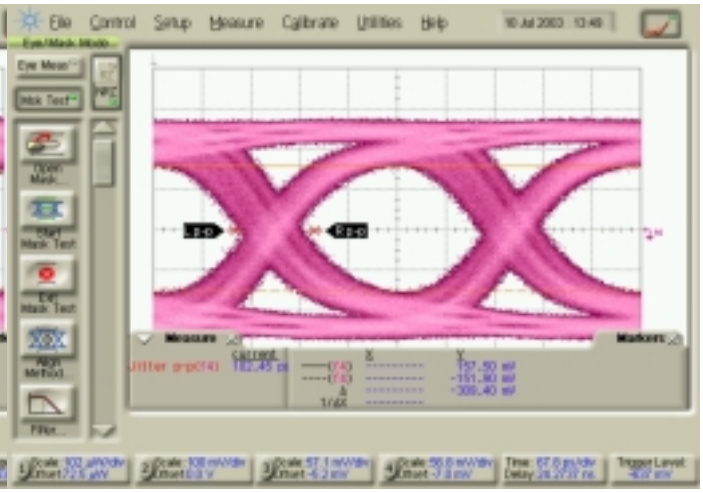
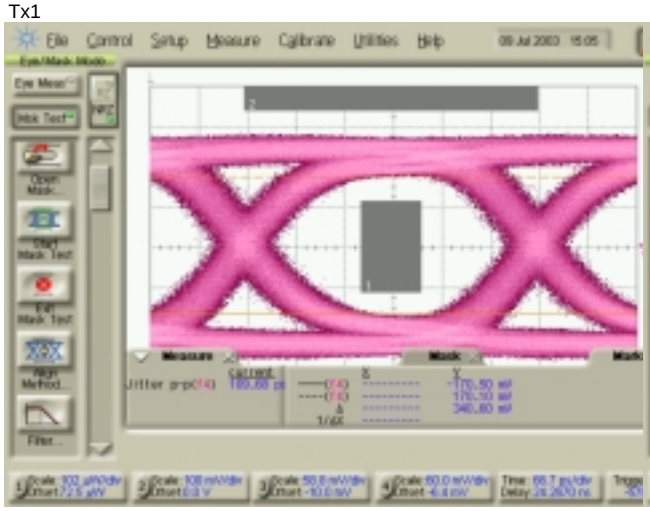


o-MGC 2.5Gbps 300m

Tx1



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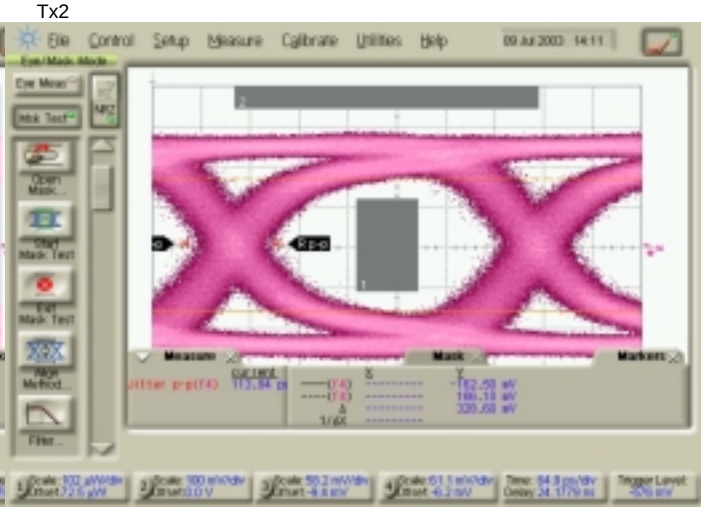

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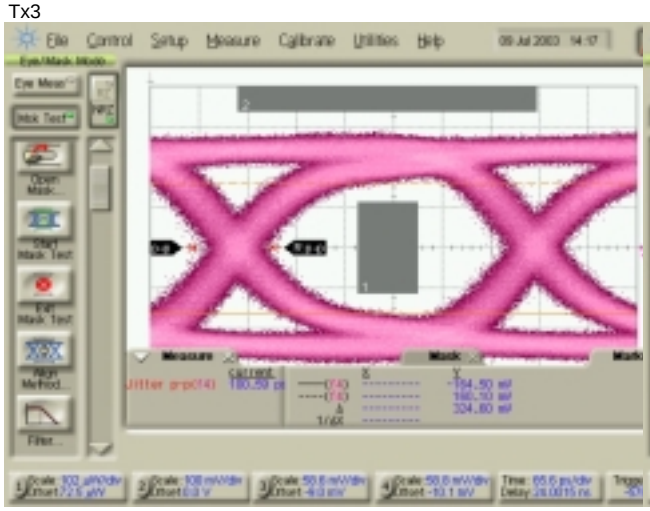

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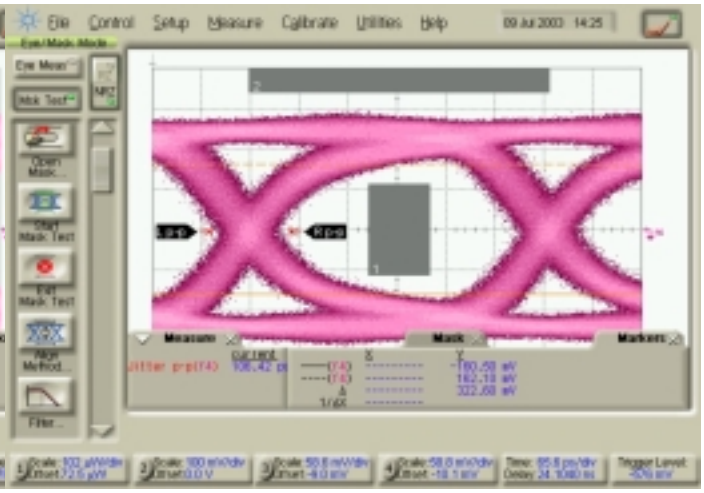
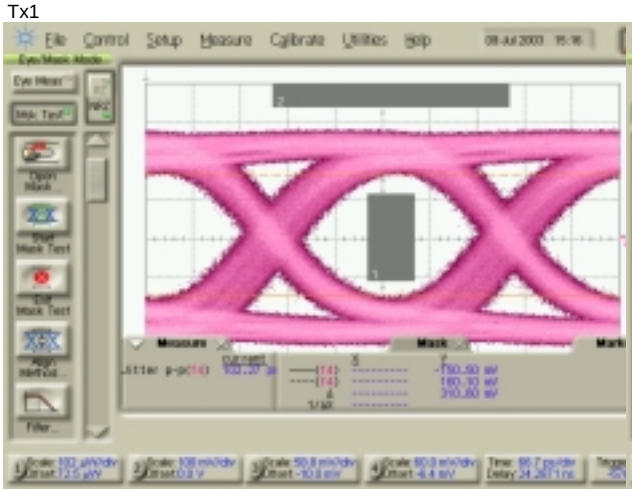


o-MGC 3Gbps 100m

Tx1



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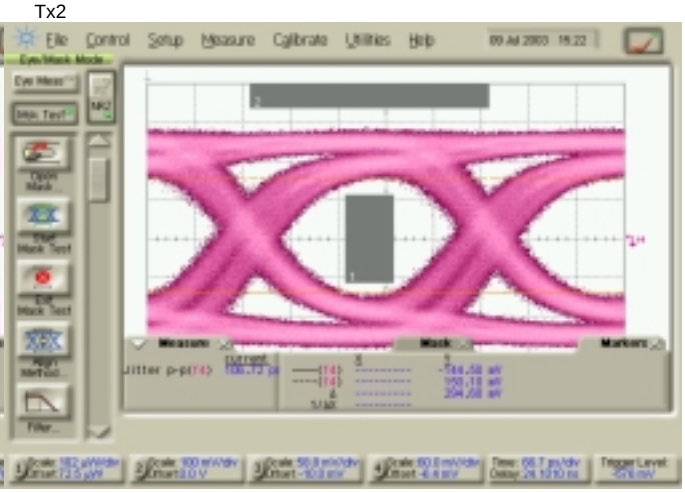

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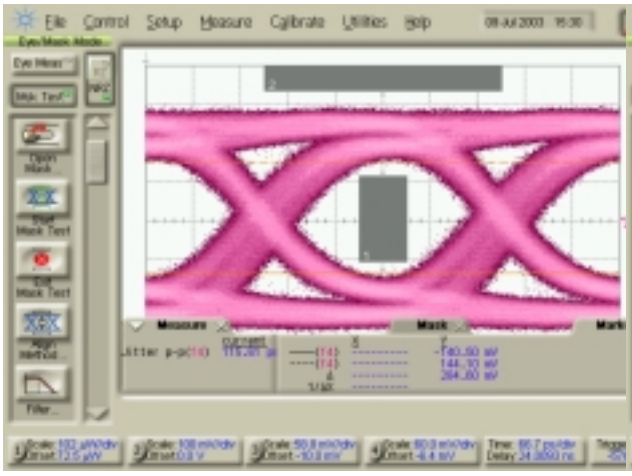

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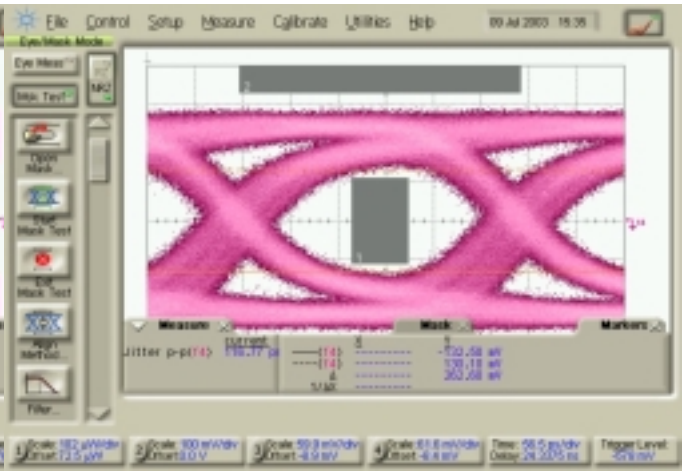
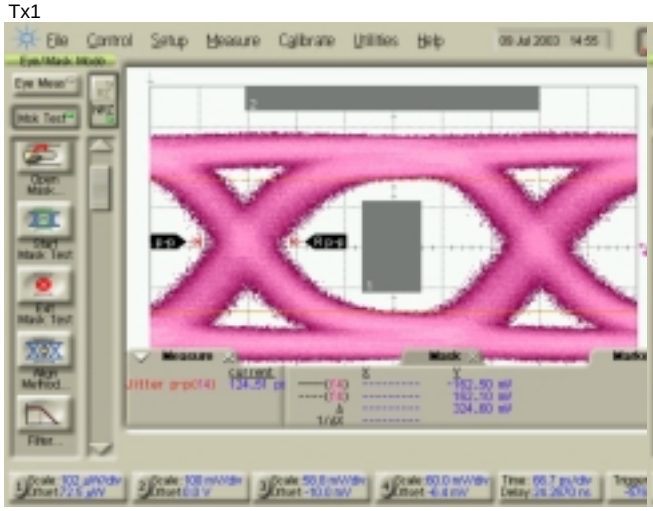
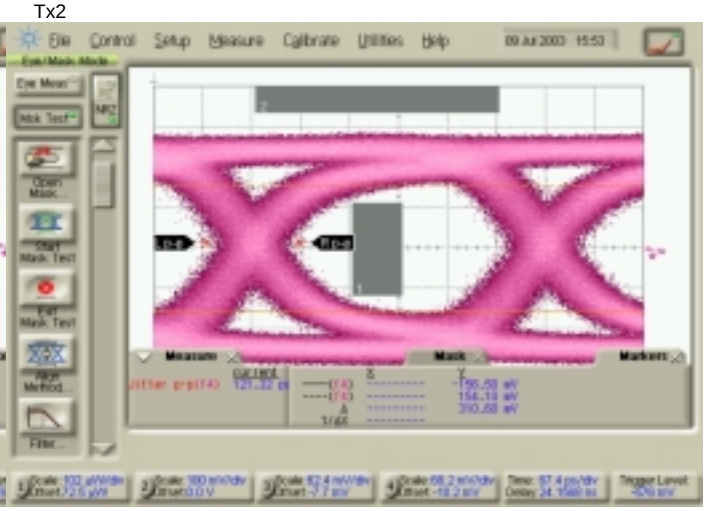


o-MGC 2.5Gbps 300m multi drive

Tx1



Tx2



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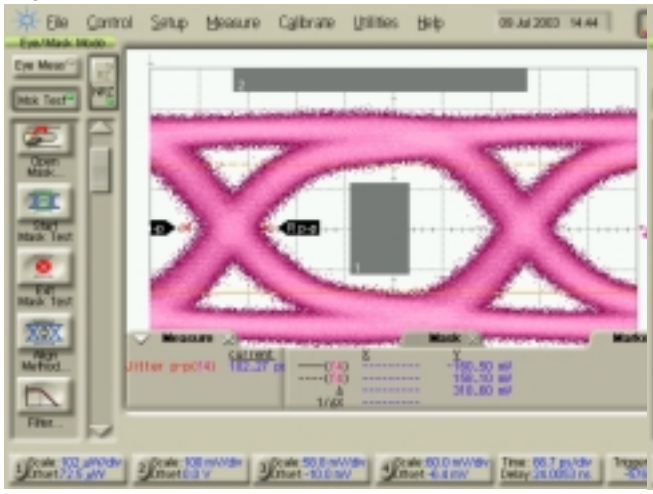

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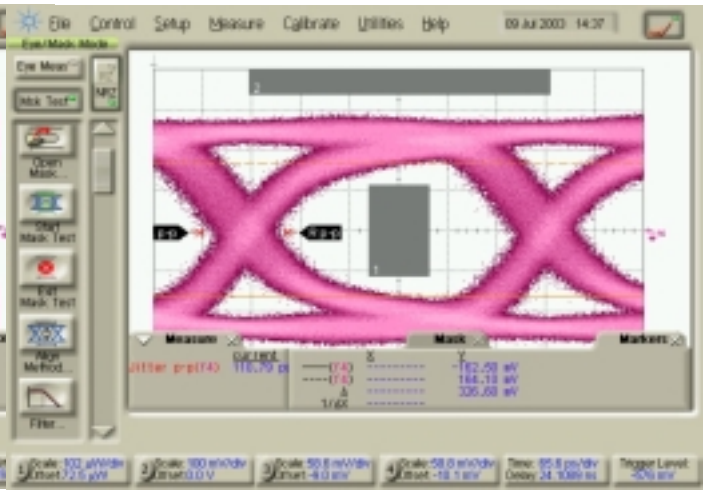
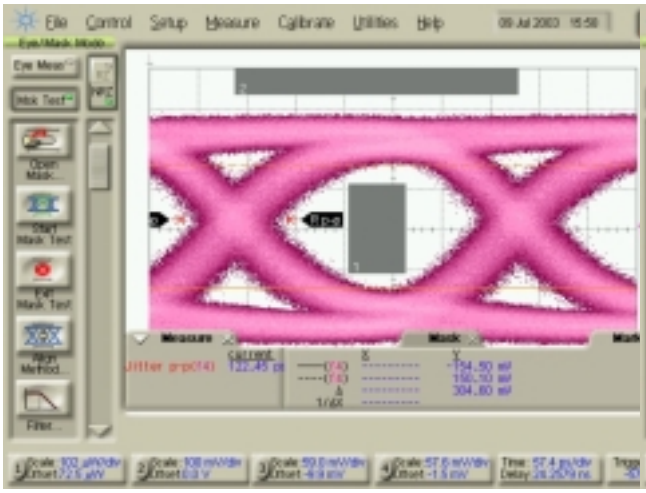


o-MGC 3Gbps 100m multi drive

Tx1



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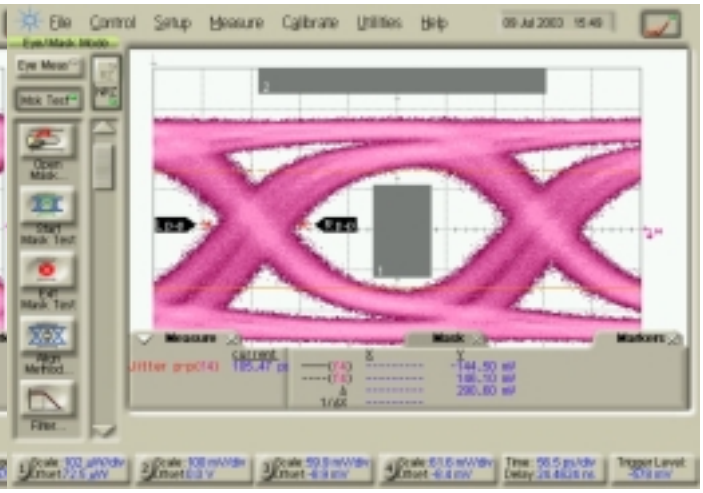

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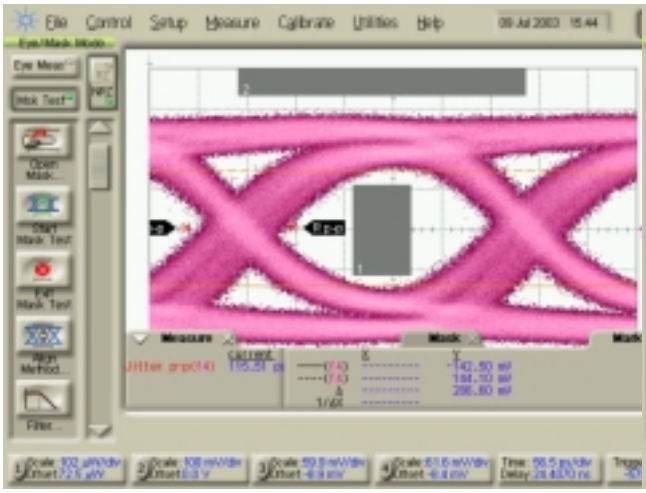

Tx4