

hp 1..4, slot 1 is unused; PSU(s) may go here.

hp 5..8, slot 2 is unused; PSU(s) may go here.

hp 9..12, slot 3 is unused; PSU(s) may go here.

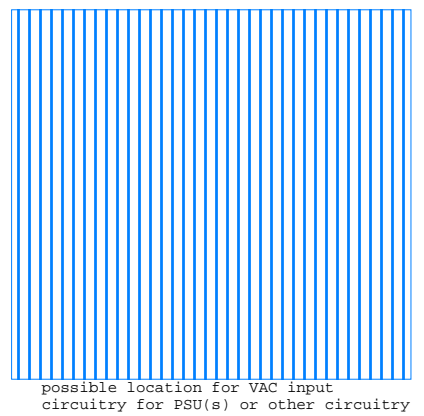
hp 13..16, slot 4 is unused; PSU(s) may go here.

Slot 1
OK to have card guides

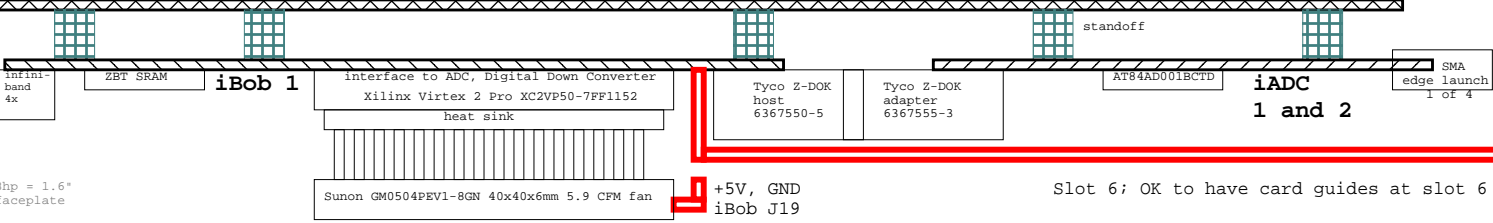
Slot 3
OK to have card guides

PSU may be independent or have a current sharing PSU backplane

Slot 3
OK to have card guides

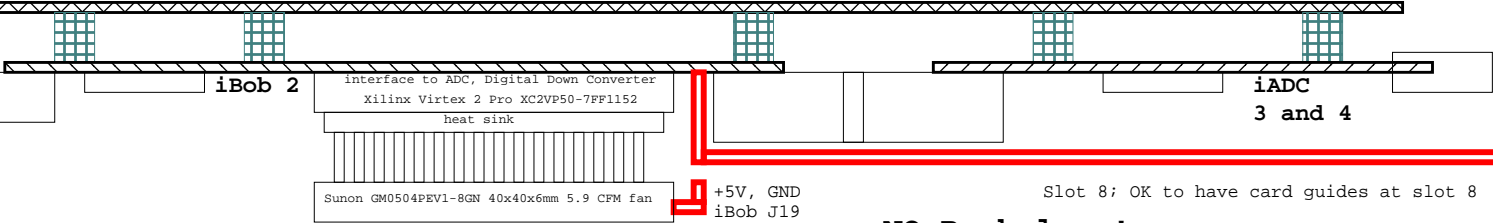


Sampler plate 1. Plate is 6U, 2mm =.08" thick, 10.275" deep



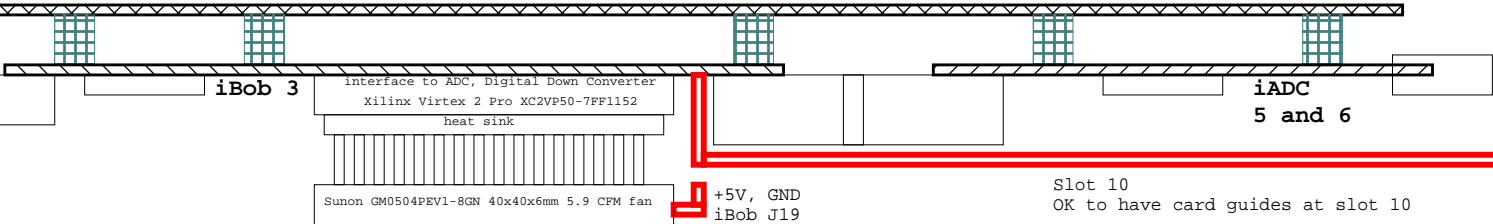
+5V, GND copper wires from PSU to iBob Sampler plate 1 has remote sense (if just 1 PSU or shared PSUs). No particular requirement for how these wires mate to the PSU.

Sampler plate 2



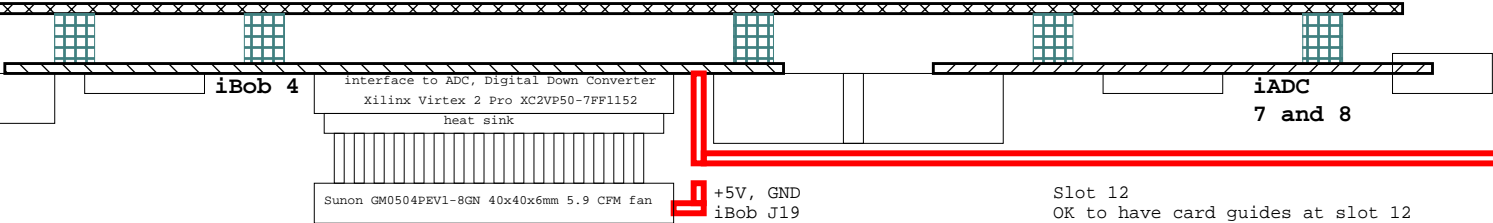
Note regarding card guides: They must accept 2mm or .08" wide cards.

Sampler plate 3



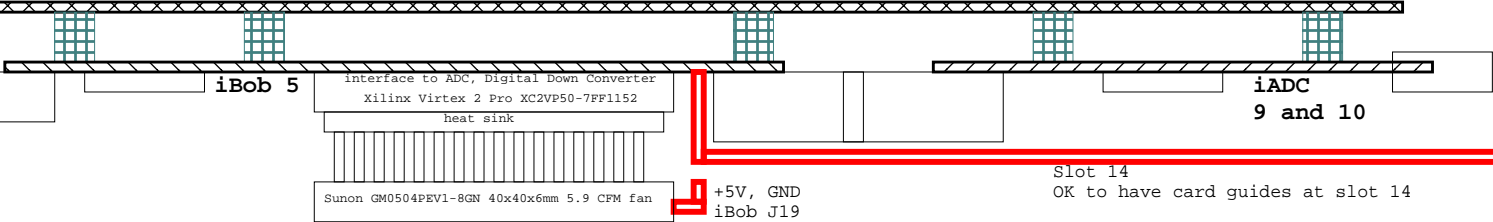
It's OK to have standard 160 mm front guides aligned with standard 80mm rear guides
It's also OK to have 1 very long guide that reaches from front to back.

Sampler plate 4.

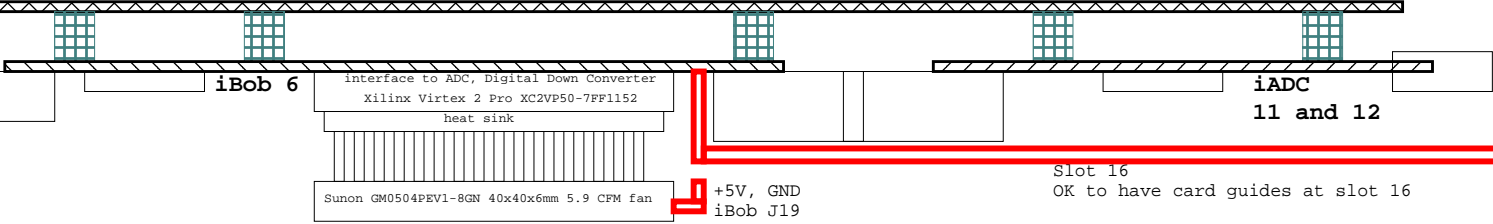


Each Sampler plate has its own +5V, GND wires to/from the PSU. Not all connections shown for clarity.

Sampler plate 5



Sampler plate 6



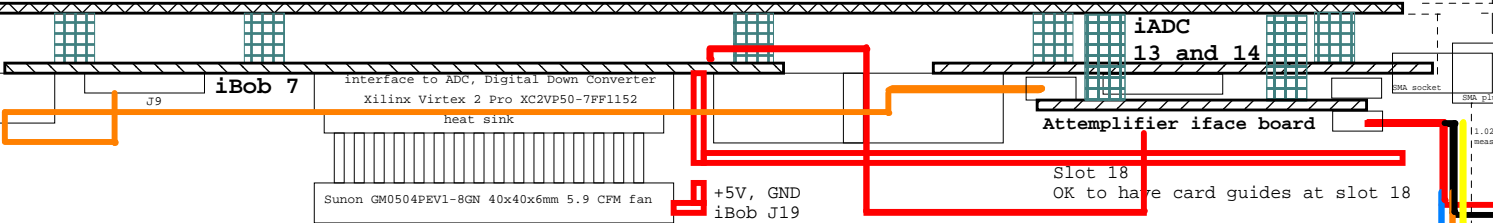
iADC board fabrication thickness : .059" +/- .006"

between the bottom of the iADC pcb and the top of the AL plate are 0.250" long #4-40 hex threaded spacers.

The AL plates are 2.0mm =.079" thick.

The plate to plate pitch is 8hp = 1.6"

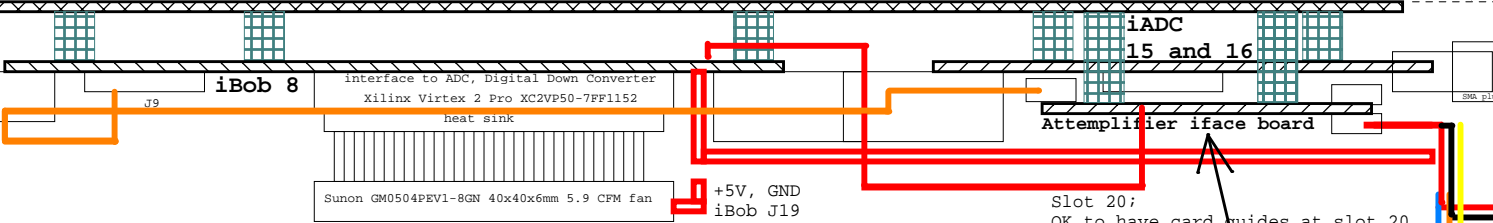
Sampler plate 7



2008feb20: NO gap required here.

we want to have some gap here

Sampler plate 8



hp 81.84 slot 21 is unused.

NO Backplane!

Slot 21; OK to have card guides at slot 21

actually: all the wires will be clumped together at the bathtub outlet; not shown here for clarity.

Actually, the Attenuator interface board is in the same plane as the iADCs. Shown separate here for clarity. The cable for the FPGA reset button isn't shown here.